| L Number | Hits | Search Text | DB | Time stamp |
|---|---|---|---|---|
| - | 241 | (712/229).CCLS. | USPAT; US-PGPUB; EPO; JPO; IBM_TDB | 2004/05/26 15:09 |
| - | 189 | (712/43).CCLS. | USPAT; US-PGPUB; EPO; JPO; IBM_TDB | 2004/05/26 15:06 |
| - | 357 | mode adj1 indicators | USPAT; US-PGPUB; JPO; DERWENT | 2004/05/26 15:07 |
| - | 63 | (operating adj1 mode) and (mode adj1 indicators) | USPAT; US-PGPUB; JPO; DERWENT | 2004/05/26 15:08 |
| - | 63 | (operating adj1 mode) and (mode adj1 indicators) | USPAT; US-PGPUB; JPO; DERWENT | 2004/05/26 15:08 |
| - | 26 | processor and (operating adj1 mode) and (mode adj1 indicators) | USPAT; US-PGPUB; JPO; DERWENT | 2004/05/26 15:08 |
| - | 8 | (operating adj1 mode) near4 (mode adj1 indicators) | USPAT; US-PGPUB; JPO; DERWENT | 2004/05/26 15:08 |
| - | 231 | (operat$4 adj1 mod$2) near4 (mod$2 adj1 bit$1) | USPAT; US-PGPUB; JPO; DERWENT | 2004/05/26 15:09 |
| - | 113 | (operating adj1 mode) and (("16" adj1 bits) near4 ("32" adj1 bits)) | USPAT; US-PGPUB; JPO; DERWENT | 2004/05/26 15:09 |
| - | 52 | (operating adj1 mode) near4 indicators | USPAT; US-PGPUB; JPO; DERWENT | 2004/05/26 15:09 |
| - | 81 | (operating adj1 mode$1) and (mode adj1 indicators) | USPAT; US-PGPUB; JPO; DERWENT | 2004/05/26 15:09 |
| - | 42 | processor and (operating adj1 mode$1) and (mode adj1 indicators) | USPAT; US-PGPUB; JPO; DERWENT | 2004/05/26 15:09 |
| - | 41 | (operating adj1 mode$1) and ((address adj1 size) and (operand adj1 size)) | USPAT; US-PGPUB; JPO; DERWENT | 2004/05/26 15:09 |
| - | 3 | (segment adj1 selector$1) near4 (entries) | USPAT; US-PGPUB; EPO; JPO; IBM_TDB | 2004/05/26 15:13 |
| - | 0 | (adjacent adj1 memory adj1 locations) near4 (64?bit) | USPAT; US-PGPUB; EPO; JPO; IBM_TDB | 2004/05/26 15:16 |

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| - | 0 | (adjacent adj1 memory) near4 (64?bit) | USPAT; US-PGPUB; EPO; JPO; IBM_TDB | 2004/05/26 15:15 |
|---|---|---|---|---|
| - | 0 | (adjacent adj1 memory) near4 ("64" adj1 bit) | USPAT; US-PGPUB; EPO; JPO; IBM_TDB | 2004/05/26 15:16 |
| - | 35 | (memory adj1 locations) near4 ("64" adj1 bit) | USPAT; US-PGPUB; EPO; JPO; IBM_TDB | 2004/05/26 15:15 |
| - | 335 | adjacent adj1 memory adj1 locations | USPAT; US-PGPUB; EPO; JPO; IBM_TDB | 2004/05/26 15:16 |
| - | 0 | (adjacent adj1 memory adj1 locations) near4 (large adj1 data) | USPAT; US-PGPUB; EPO; JPO; IBM_TDB | 2004/05/26 15:31 |
| - | 73 | (adjacent adj1 memory adj1 locations) near4 (data) | USPAT; US-PGPUB; EPO; JPO; IBM_TDB | 2004/05/26 15:18 |
| - | 27 | (floating adj1 point) near4 (memory adj1 locations) | USPAT; US-PGPUB; EPO; JPO; IBM_TDB | 2004/05/26 15:19 |
| - | 0 | (memory adj1 locations) near4 (large adj1 data) | USPAT; US-PGPUB; EPO; JPO; IBM_TDB | 2004/05/26 15:32 |
| - | 7 | (memory adj1 locations) with (large adj1 data) | USPAT; US-PGPUB; EPO; JPO; IBM_TDB | 2004/05/26 15:36 |
| - | 26 | (memory adj1 locations) same (large adj1 data) | USPAT; US-PGPUB; EPO; JPO; IBM_TDB | 2004/05/26 15:47 |
| - | 120 | (double adj1 word) near4 storage | USPAT; US-PGPUB; EPO; JPO; IBM_TDB | 2004/05/26 15:52 |
| - | 11 | (double adj1 word) near4 storage near4 locations | USPAT; US-PGPUB; EPO; JPO; IBM_TDB | 2004/05/26 15:52 |
| - | 827 | instruction adj1 translat$3 | USPAT; US-PGPUB; EPO; JPO; IBM_TDB | 2004/05/26 16:02 |
| - | 4 | software near4 instruction adj1 translat$3 | USPAT; US-PGPUB; EPO; JPO; IBM_TDB | 2004/05/26 16:02 |
| - | 0 | software adj1 instruction adj1 translat$3 | USPAT; US-PGPUB; EPO; JPO; IBM_TDB | 2004/05/26 16:02 |
| - | 13 | software near4 (instruction adj1 translat$3) | USPAT; US-PGPUB; EPO; JPO; IBM_TDB | 2004/05/26 16:02 |

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